


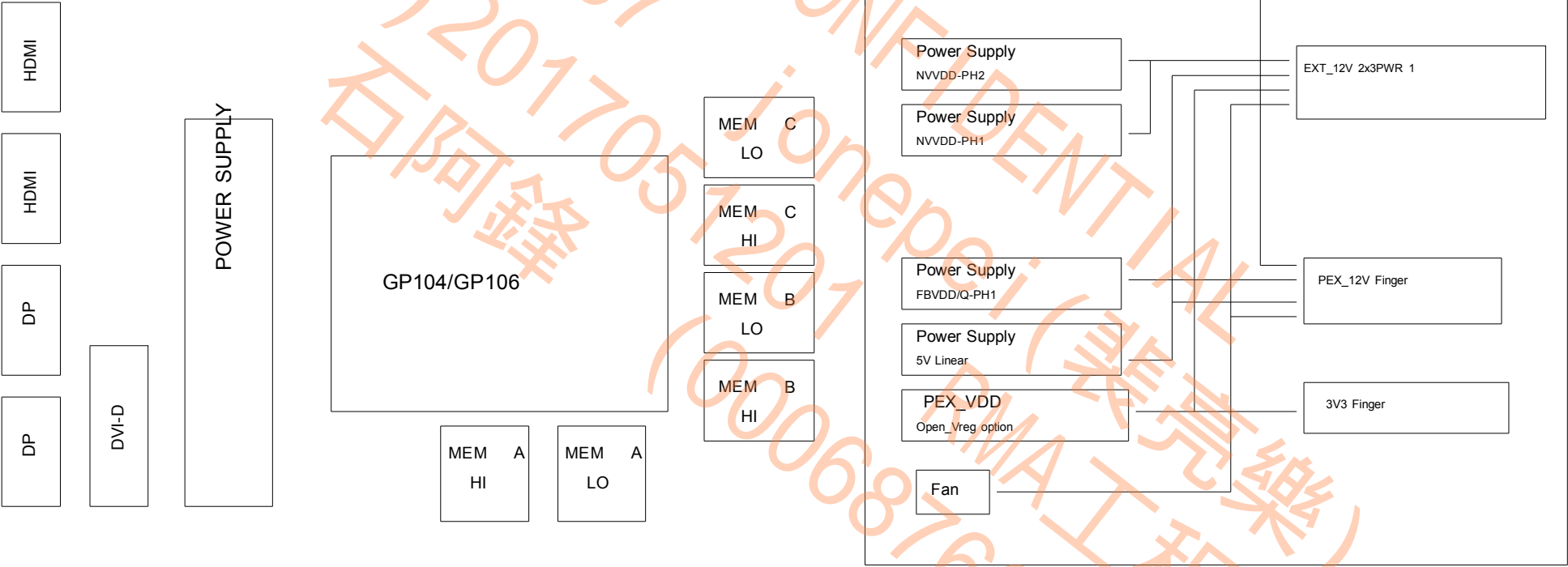
# PG410 A01

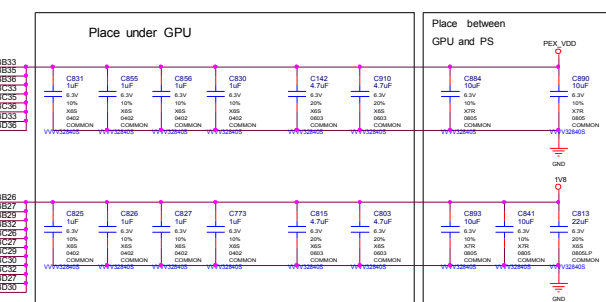
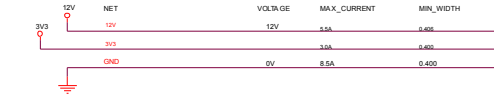
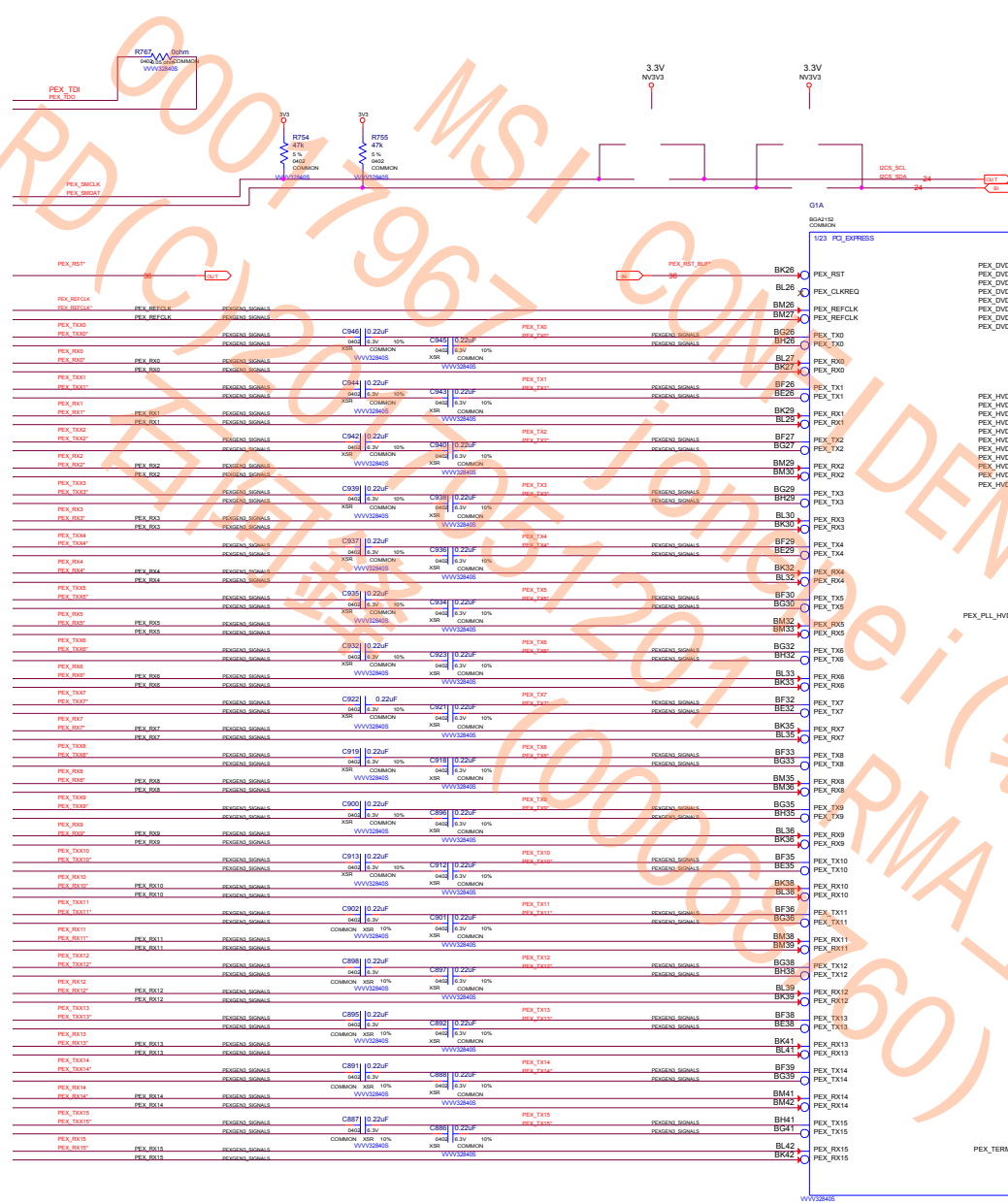
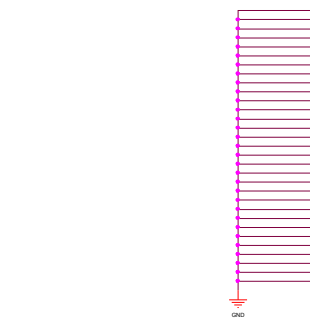
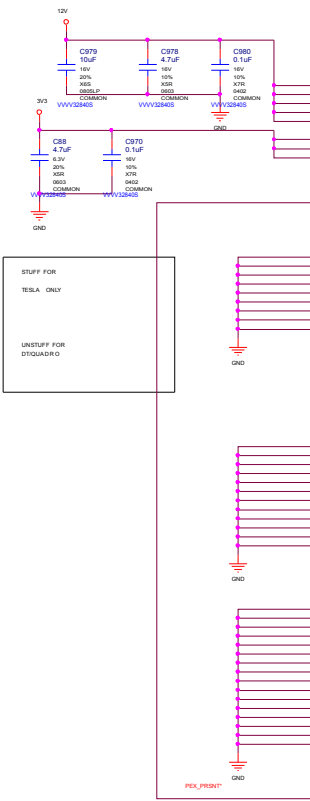
GP106 8GB/4GB GDDR5, 256b, 256Mx32/128MX32  
Tall DVI-D + DP + DP + HDMI + HDMI

## TABLE OF CONTENTS

Page	Description	Page	Description
1	Table of Contents	26	PS: 1V8_PLL, 1V8_AON
2	Block Diagram	27	PS: 5V, PEX_VDD
3	PCI Express	28	PS: NV3V3, NV12V
4	GPU FB_AB	29	PS: FBVDD
5	MEMORY: FBA[31:0]	30	PS: NVVDD Controller
6	MEMORY: FBA[63:32]	31	PS: NVVDD Phase 1,2 & 3
7	MEMORY: FBB[31:0]	32	PS: NVVDD Rail Balance Login & Dynamic Phase
8	MEMORY: FBB[63:32]	33	PS: Input, Filtering, and Monitoring
9	MEM FB_AB PWR	34	PS: 12V Power Steering,PSI Control & LED
10	GPU FB_CD	35	PS: Shut Down and Sequencing
11	MEMORY: FBC[31:0]	36	PS: GC6 MISC
12	MEMORY: FBC[63:32]	37	MECH
13	MEMORY: FBD[31:0]	38	Change History
14	MEMORY: FBD[63:32]		
15	MEM FB_CD PWR		
16	GPU PWR & GND		
17	GPU Decoupling		
18	IFPAB DVI-D-DL		
19	IFPE DP		
20	IFPF DP		
21	IFPC HDMI		
22	IFPD HDMI		
23	MIOA/B Interface and Frame Lock		
24	MISC1: Fan, Thermal, JTAG, GPIO,STEREO		
25	MISC2: ROM, XTAL,STRAPS		

				<b>MICRO-STAR INT'L CO.,LTD</b> <b>MS-V328</b>	
Size	Custom	Document	Description	Rev	7.0
Date: Wednesday, November 23, 2016				Sheet	1 of 38





GDOR5 CMD Mapping		
CMD0	0-31	32-63
CMD0	CAS*	
CMD1	CKE*	
CMD2	RST*	
CMD3	RAS*	
CMD4	A1_A0	
CMD5	A2_A10	
CMD6	A2_RFU	
CMD7	AB*	
CMD8	AS_A11	
CMD9	AT_A0	
CMD10	WE*	
CMD11	AS_BA1	
CMD12	AS_BA2	
CMD13	AS_BA3	
CMD14	AS_BA4	
CMD15	CS*	
CMD16	CAS*	
CMD17	CKE*	
CMD18	RST*	
CMD19	RAS*	
CMD20	A1_A0	
CMD21	A2_A10	
CMD22	A2_RFU	
CMD23	AB*	
CMD24	AS_A11	
CMD25	AT_A0	
CMD26	WE*	
CMD27	AS_BA1	
CMD28	AS_BA2	
CMD29	AS_BA3	
CMD30	AS_BA4	
CMD31	CS*	

# G1B

SGA2152 COMMAND

320 FIB

FIB\_CMD0

FIB\_CMD1

FIB\_CMD2

FIB\_CMD3

FIB\_CMD4

FIB\_CMD5

FIB\_CMD6

FIB\_CMD7

FIB\_CMD8

FIB\_CMD9

FIB\_CMD10

FIB\_CMD11

FIB\_CMD12

FIB\_CMD13

FIB\_CMD14

FIB\_CMD15

FIB\_CMD16

FIB\_CMD17

FIB\_CMD18

FIB\_CMD19

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FIB\_CMD133

FIB\_CMD134

FIB\_CMD135

FIB\_CMD136

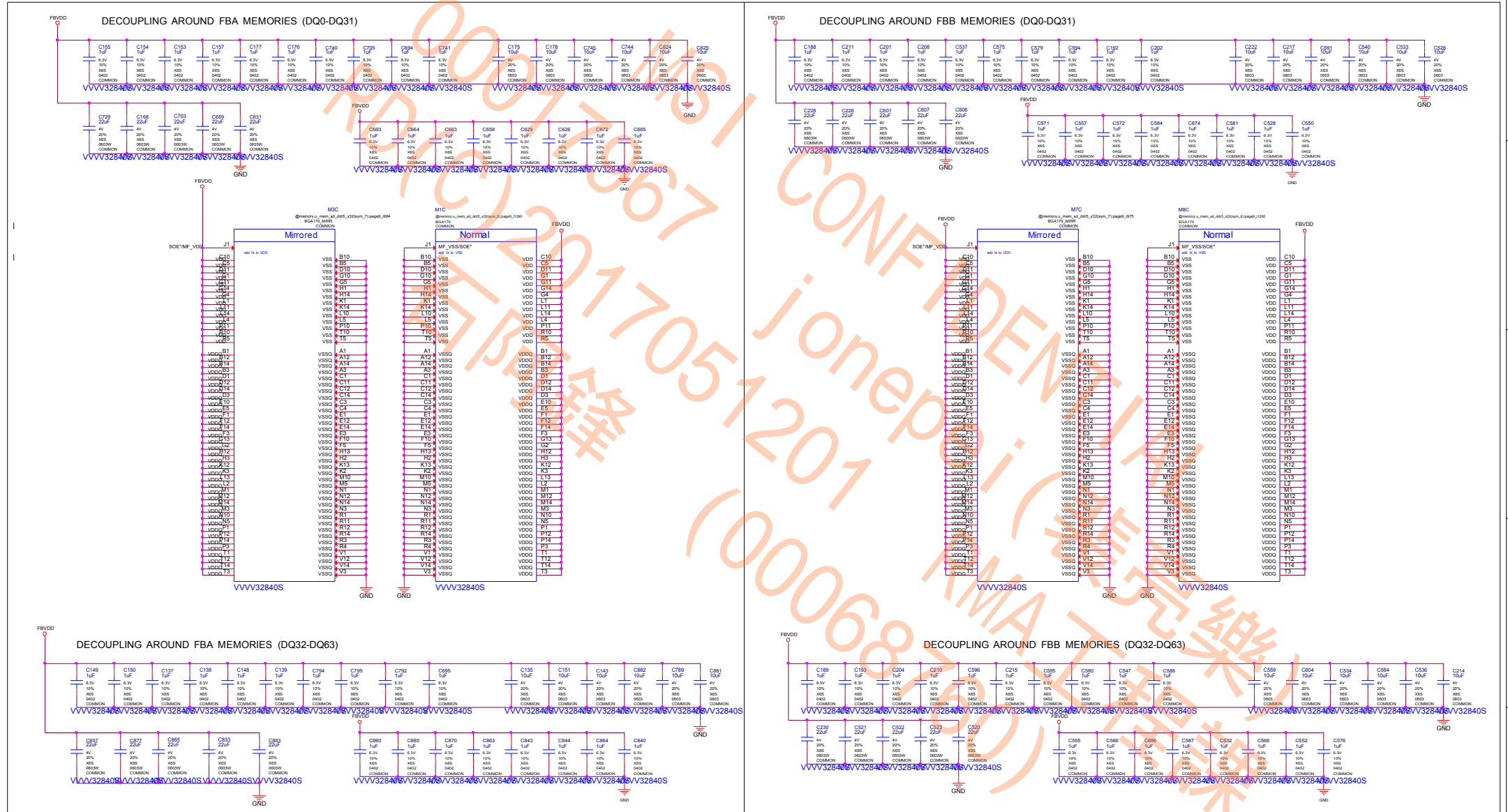






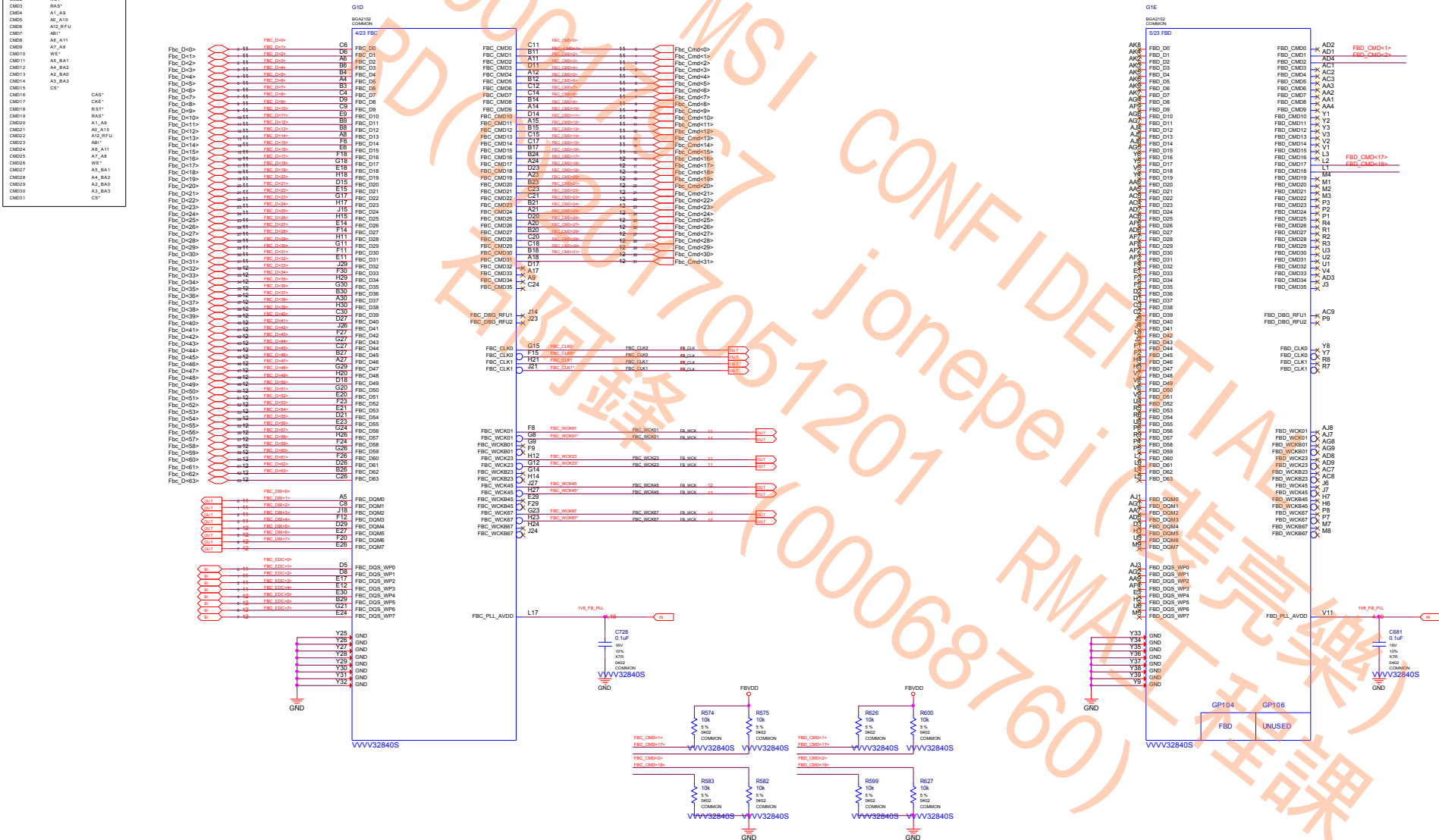






GDOR5 CMD Mapping


CMD	0-31	32-63
CMD0	CAS*	
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CMD5	AS_A10	
CMD6	AD3_RFU	
CMD7	AB1*	
CMD8	AS_A11	
CMD9	A7_A0	
CMD10	WE*	
CMD11	AS_BA1	
CMD12	AS_BA2	
CMD13	A2_BA0	
CMD14	A3_BA3	
CMD15	CS*	
CMD16		CAS*
CMD17		CKE*
CMD18		RST*
CMD19		AB1*
CMD20	A1_A0	
CMD21	AS_A10	
CMD22	AD3_RFU	
CMD23	AB1*	
CMD24	AS_A11	
CMD25	A7_A0	
CMD26	WE*	
CMD27	AS_BA1	
CMD28	AS_BA2	
CMD29	A2_BA0	
CMD30	A3_BA3	
CMD31	CS*	







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	MICRO-STAR INT'L CO.,LTD		
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	Size	Document	Description
	Custom	13	Rev 7.0
Date: Wednesday, November 23, 2016			Sheet 13 of 38

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Date: Wednesday, November 23, 2016		Sheet 14 of 38	





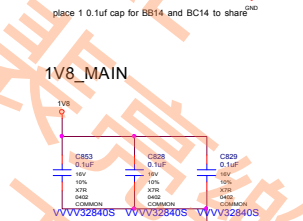
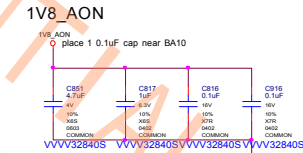
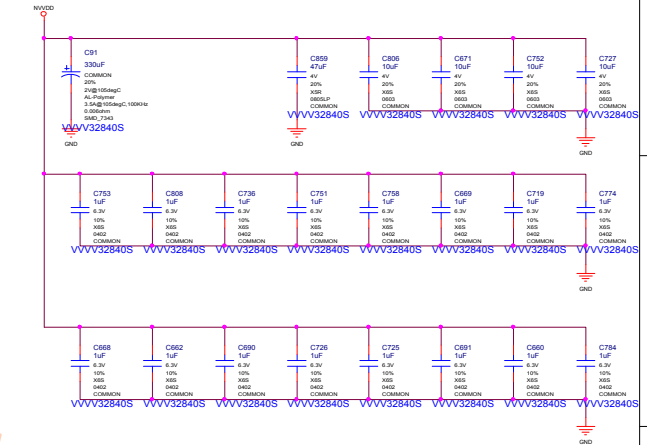
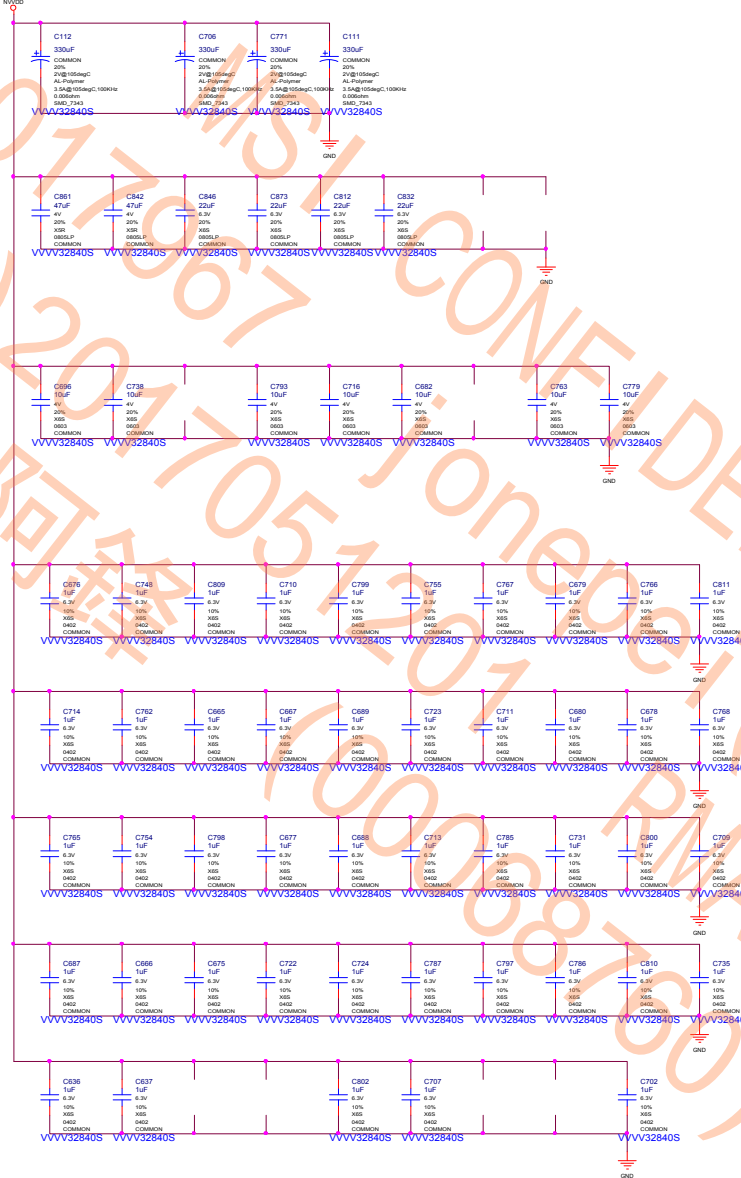
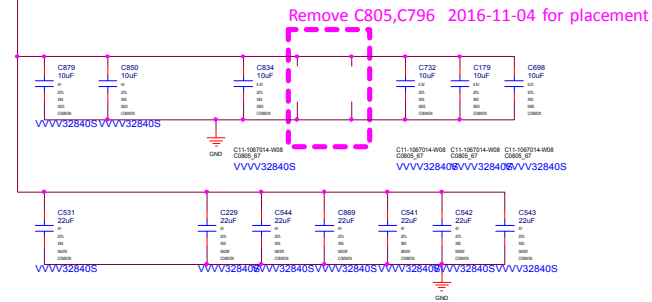
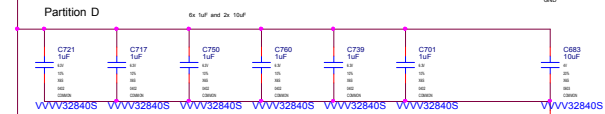
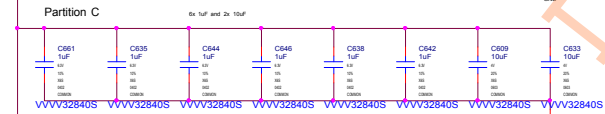
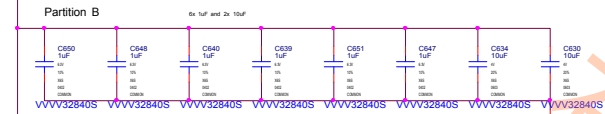
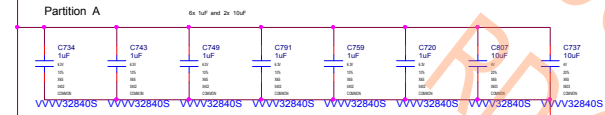


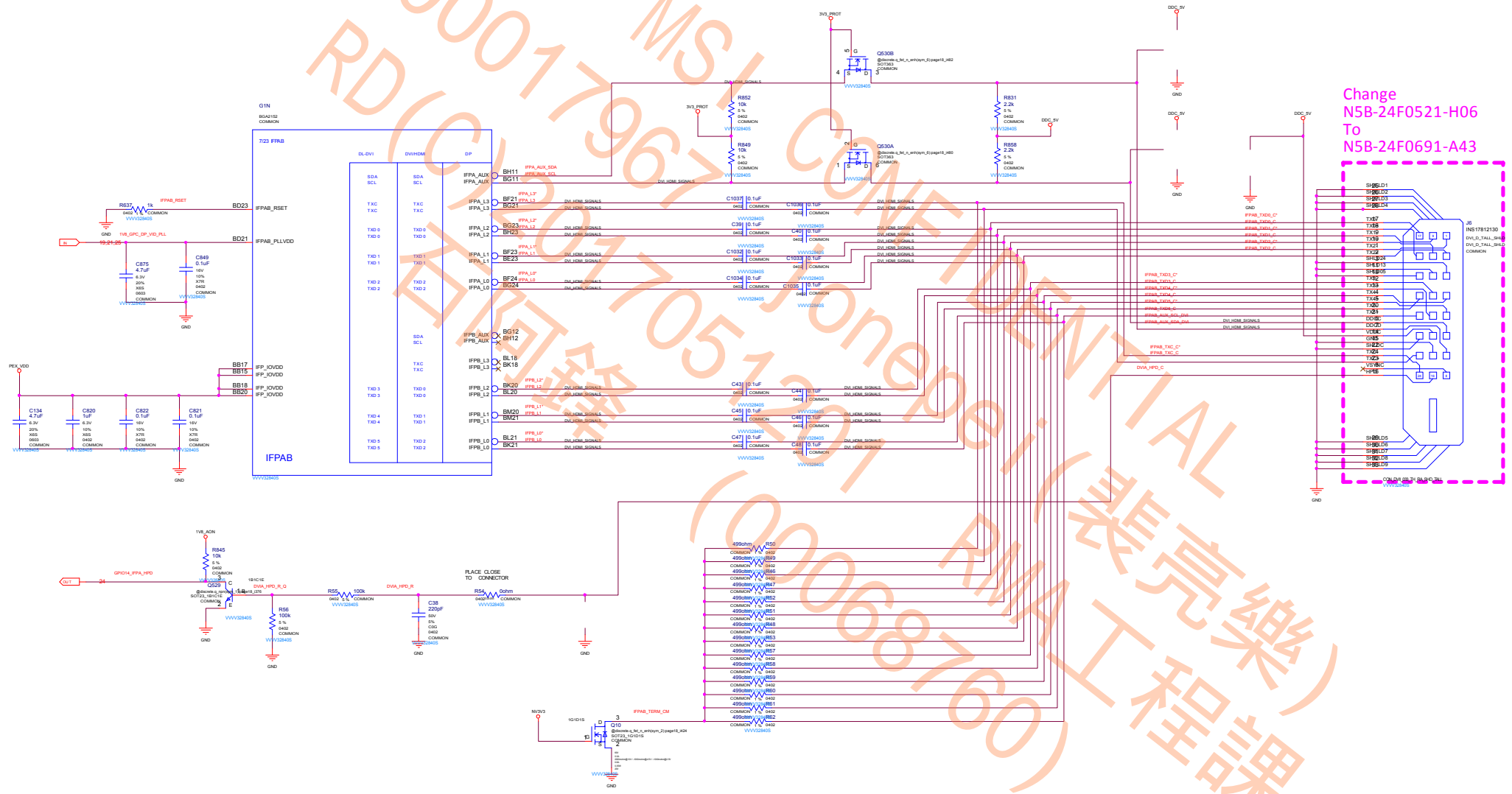


## FBVDD

## NVDD

## NVDD



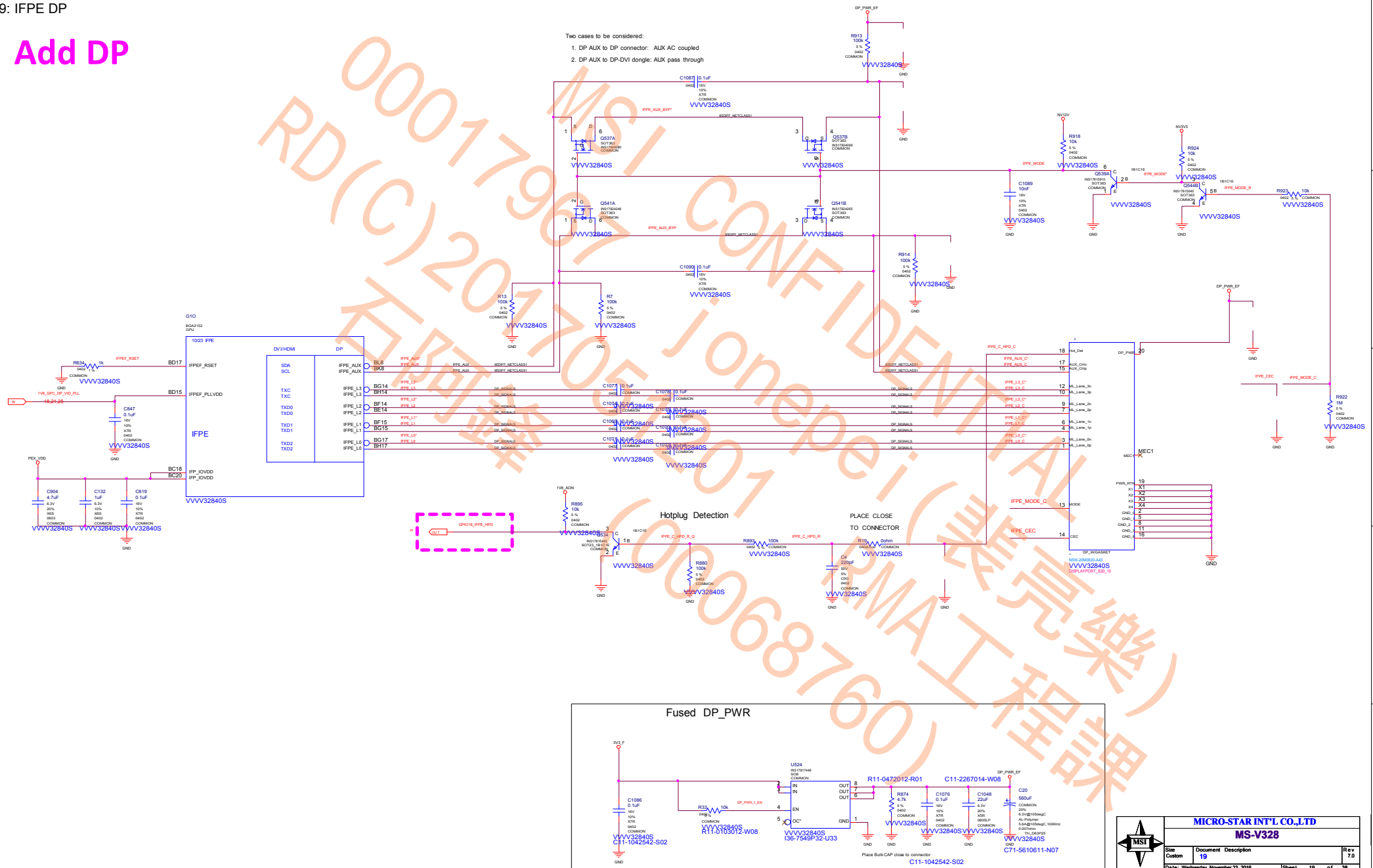


Change  
N5B-24F0521-H06  
To  
N5B-24F0691-A43

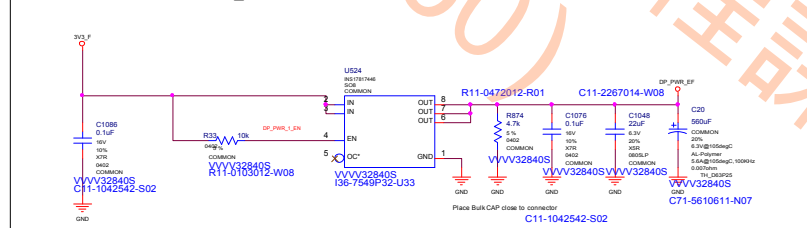
## Add DP

Two cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



## Fused DP\_PWR

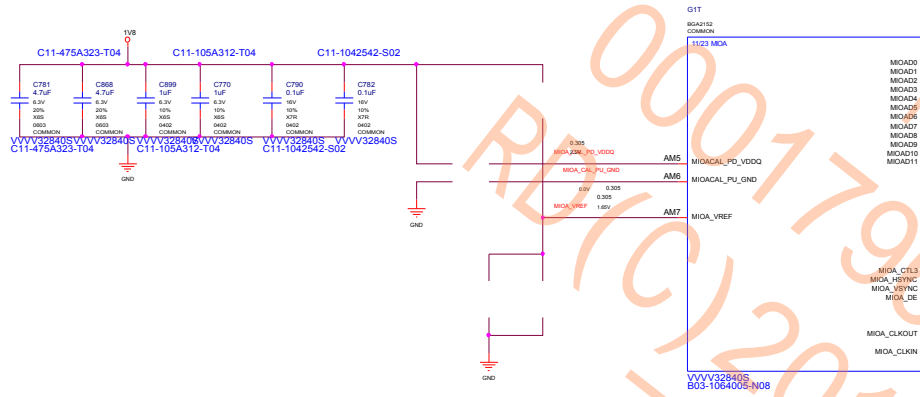


MICRO-STAR INT'L CO., LTD		
MS-V328		
Size	Document	Rev
Custom	19	7.0
Date: Wednesday, November 23, 2016		
Sheet 19 of 38		

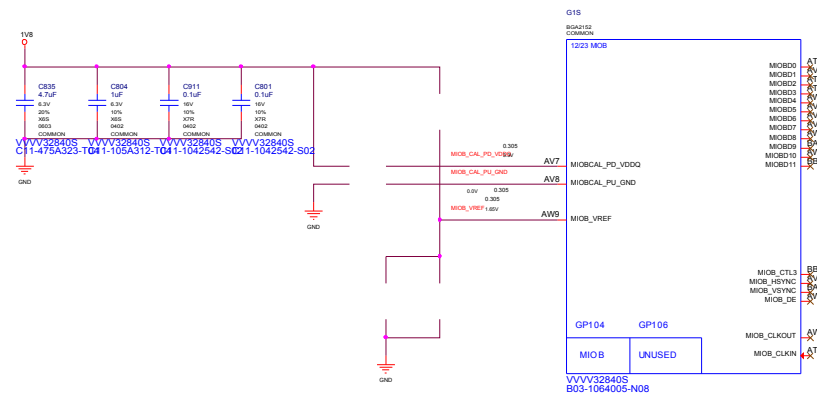








Pad Overlap R1042 & R1043

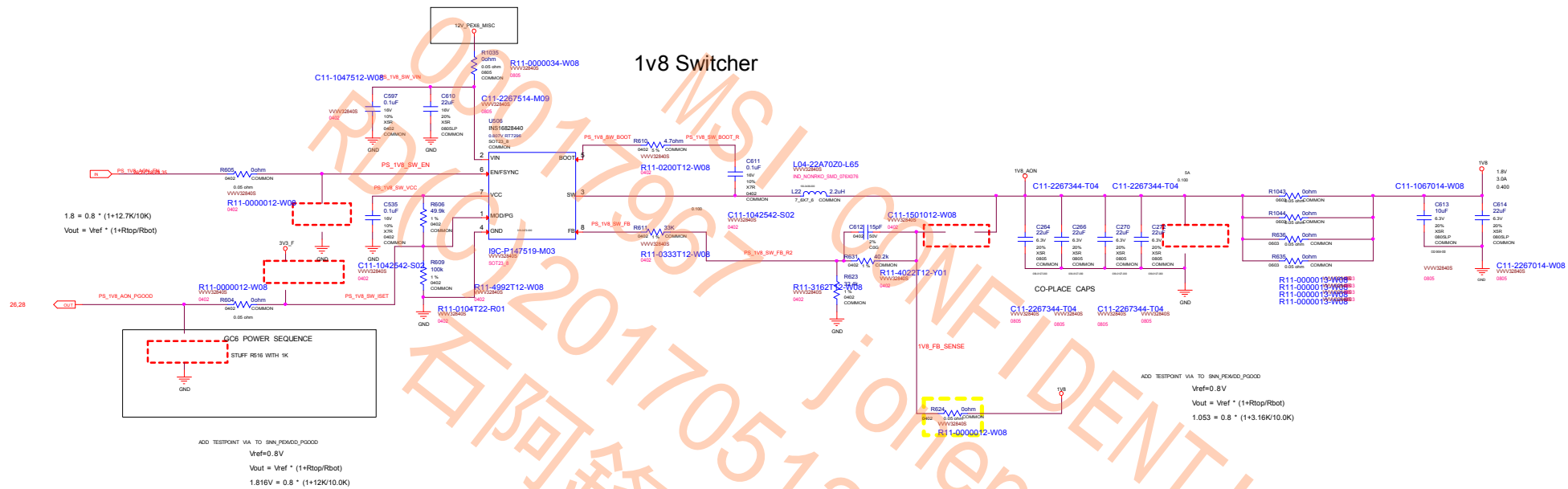


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MS-V328			
Size	Document	Description	Rev
Custom	23		7.0
Date: Wednesday, November 23, 2016		Sheet	23 of 38

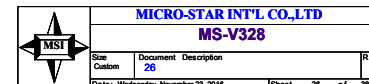






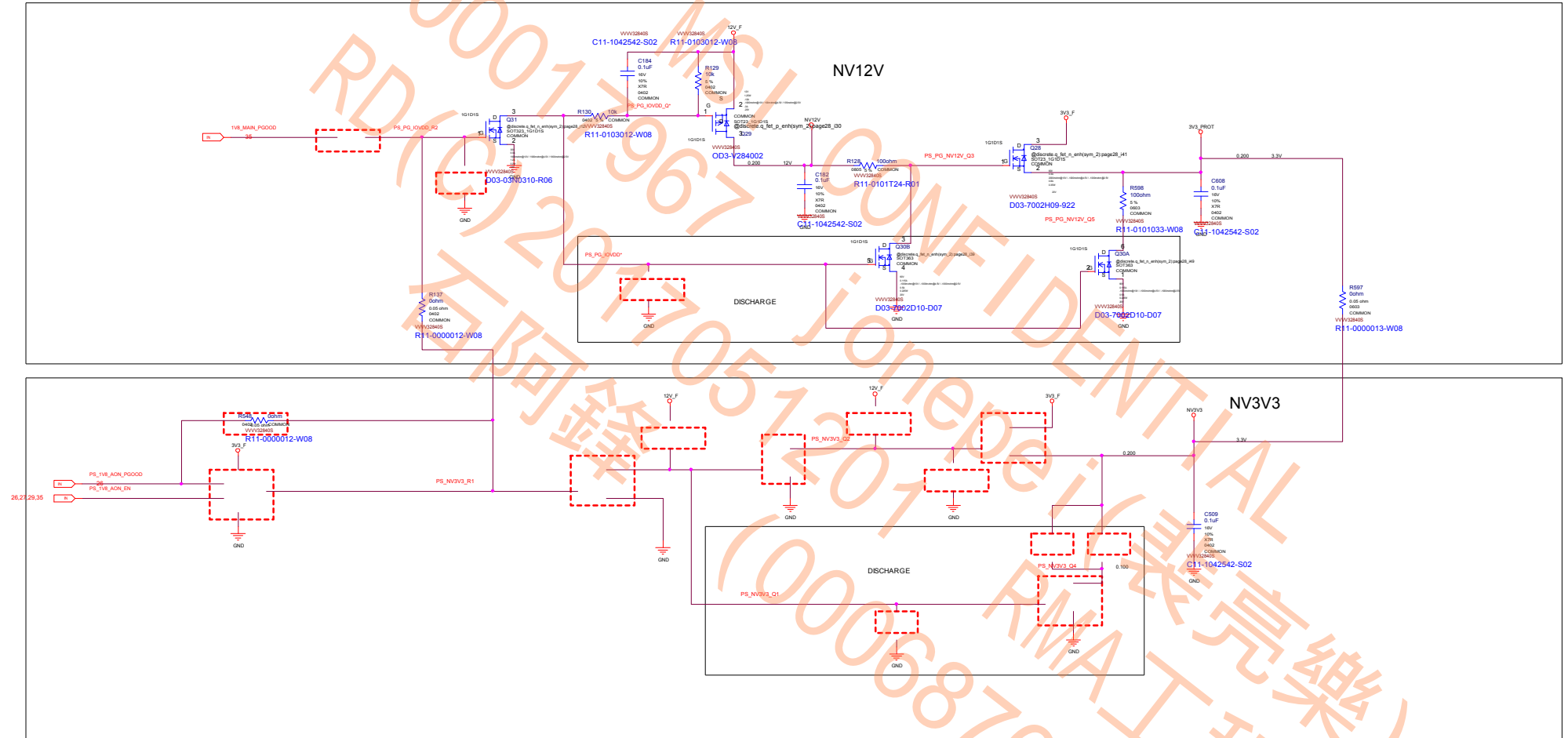


Remove 1v8 , PEX LDO



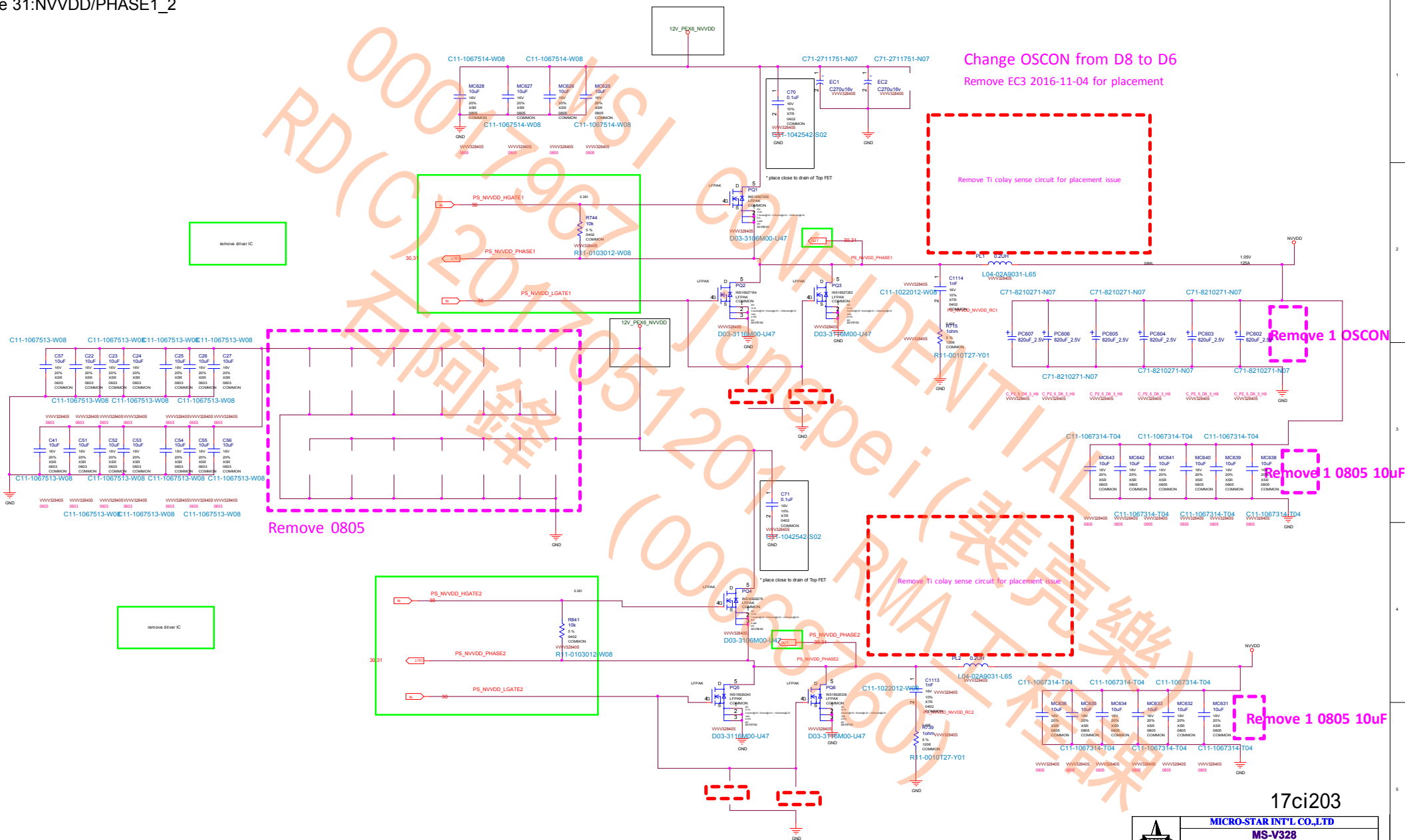
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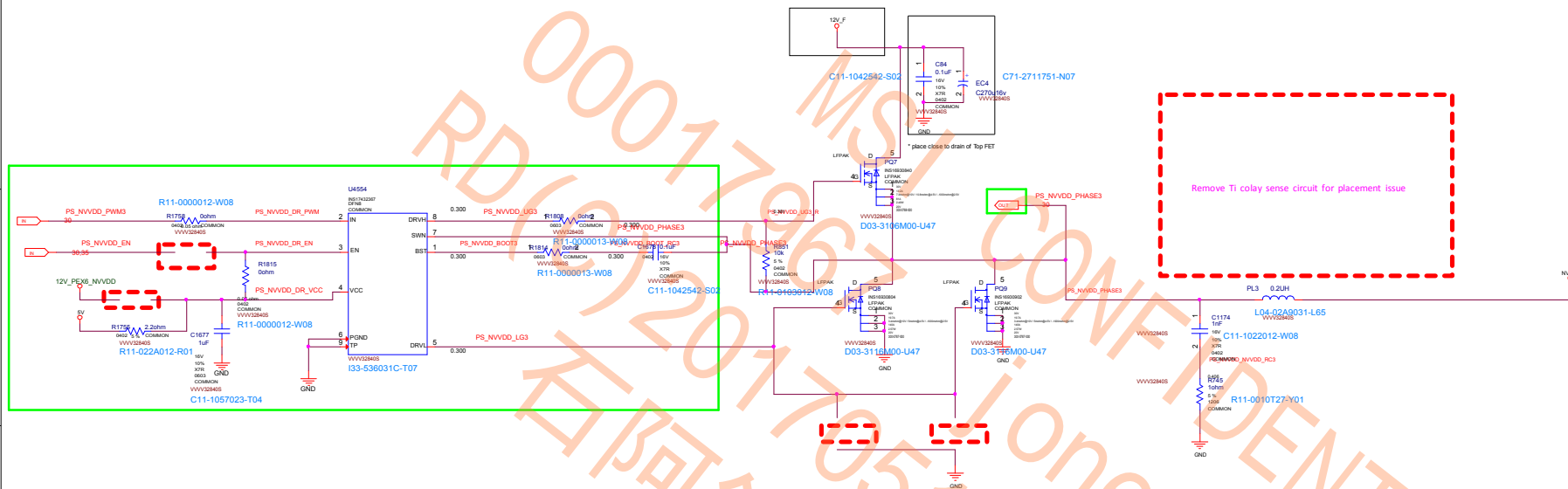








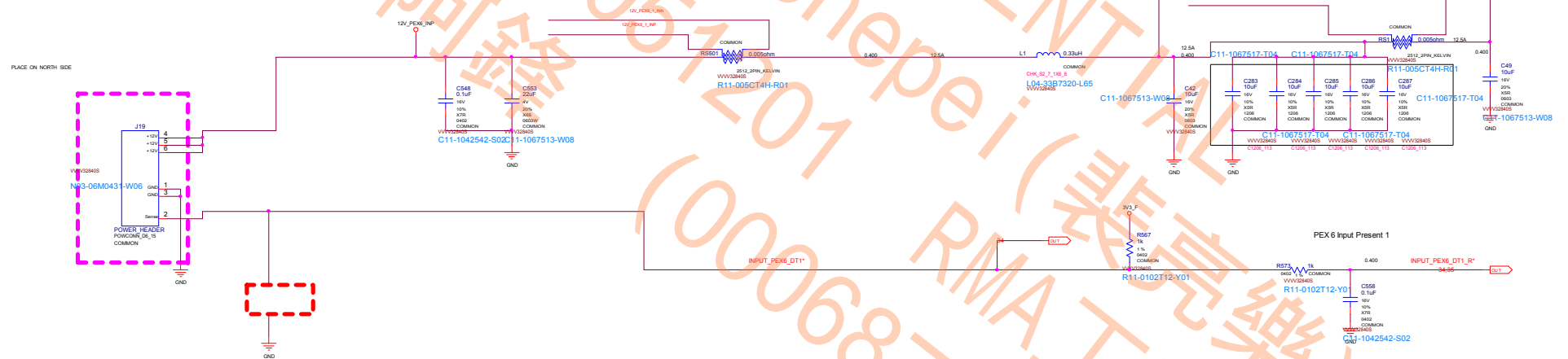
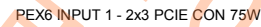
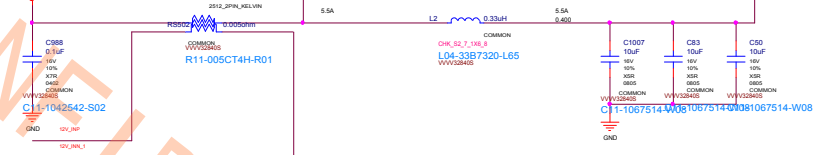
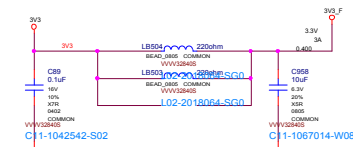
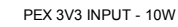
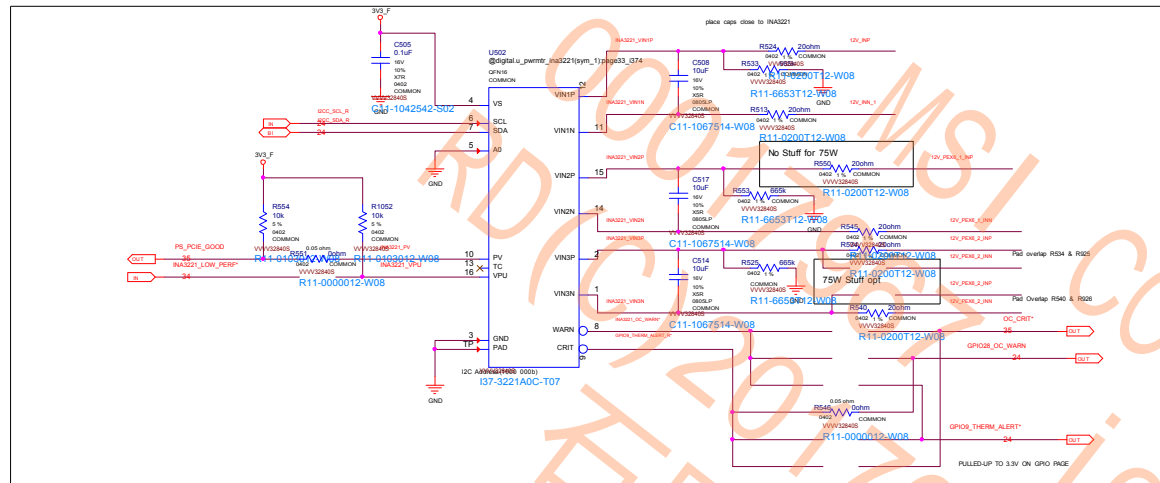




CORE 90A / 3 = 30A  
CORE 1 - EXT 6  
CORE 2 - EXT 6  
CORE 3 - BUS  
MEM 30A- BUS  
-BUS WARRING-

CORE 90A /4 =22, 5A  
CORE 1 - EXT 6  
CORE 2 - EXT 6  
CORE 3 - BUS  
CORE 4 - EXT 6  
MEM 30A- BUS





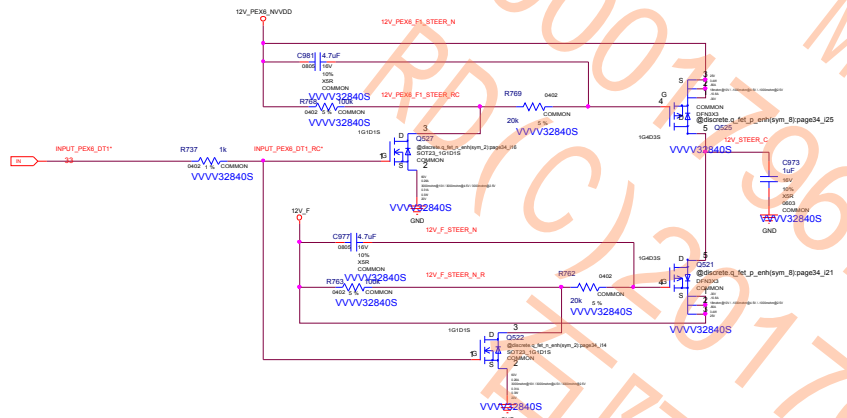
**MICRO-STAR INT'L CO.,LTD**

MS-V328

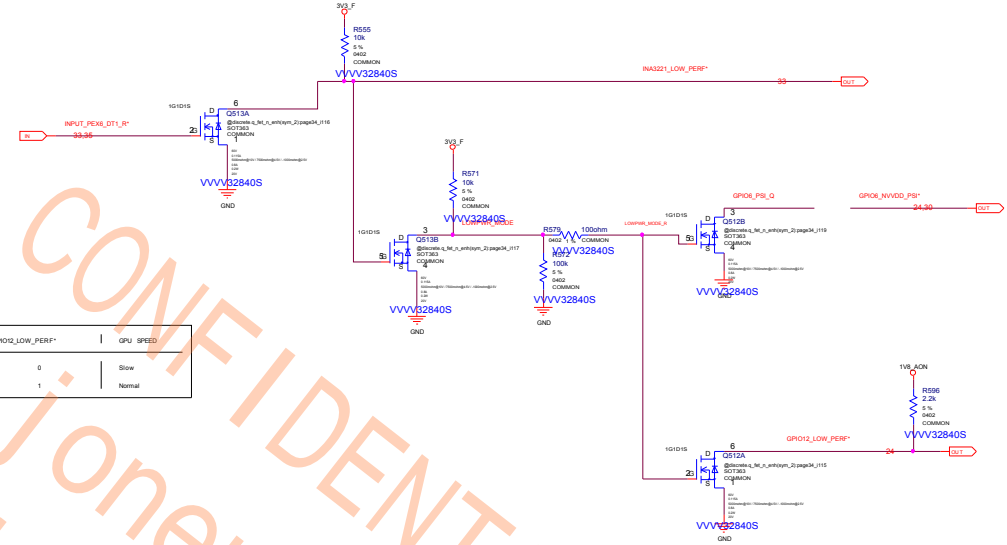
Size Custom	Document Description 33	Rev 7.0
Date: Wednesday, November 23, 2016		Sheet 33 of 38

### 12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA



### PEX Input - Power Level/PSI\* Control



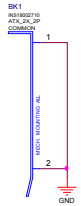
GeForce Logo LED





remove 5V LED NET

Brackets:



Remove: MEC12/MEC13/MEC14/MEC15/MEC11/MEC8/MECG2/MECG1

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MS-V328			
Size	Document	Description	Rev
Custom	37		7.0
Date: Wednesday, November 23, 2016		Sheet	37 of 38

2016-10-28

Page 18: Change DVI connector to high rise type from N5B-24F0521-H06 to N5B-24F0691-A43

Page 19: Add IFPE DP (N5W-20M0820-A43)

Page 20: Add IFPF DP (N5W-20M0820-A43)

Page 21: Change HDMI connector from N5Y-19M0760-W06 to N5Y-19M0580-W06

Page 22: Change DP to HDMI from V328-62 (N5Y-19M0580-W06)

Page 26: Remove 1v8, PEX LDO circuit, Check 1v8 with V328-31-030

Page 27: Check PEX & PEX Discharge with V328-31-030

Page 31:

Input: change OSCON from D8 to D6 and remove 0805 CoLay

circuit

Output: Remove 1 Oscon and 2 0805 10uF cap

Page 33: Change 6pin power header to reverse type (N93-06M0431-W06)

Page 17: Remove C805,C796 for placement 2016-11-04

2016-11-04

Page 31: Remove EC3 for placement

2016-11-07

Page 37: Change BKT back to 2 hole

2016-11-15

Page 29: Change PR519 from 1206 to 0805 for snubber placement

2016-11-15

Page 27: Change PEX LB11 from L04-01071K0-M26 to L04-01073P0-L65

Page 27: Change 5V L502 from L04-47A70L0-T36 to L04-47A70Q0-L65

Page 26: Change 1v8 L22 from L04-22A70W0-T15 to L04-22A70Z0-L65

MICRO-STAR INT'L CO.,LTD			
MS-V328			
Size	Document	Description	Rev
Custom	37		7.0
Date: Wednesday, November 23, 2016		Sheet	38 of 38